SC33020H
SC33020HQ
2A Load Switch with Reverse Current Blocking

POWER MANAGEMENT

Features

- Input Voltage Range – 1.6V to 5.5V
- 2A Continuous Output Current
- Ultra Low Ron – 32mΩ
- Reverse Current Blocking
- Low Quiescent Current – 1µA
- Low Shutdown Current – 0.3µA
- Internal Soft Start
- Hardened ESD Protection 4kV
- Package: CSP – 0.9mm x 0.9mm 4-Bump
- AEC-Q100 Qualified (Grade3) Version Available

Applications

- Wearable Electronics
- Tablet PCs, eReaders
- Smartphones
- Notebook PCs, Ultrabooks
- Battery Powered Equipment
- Other Portable Devices

Description

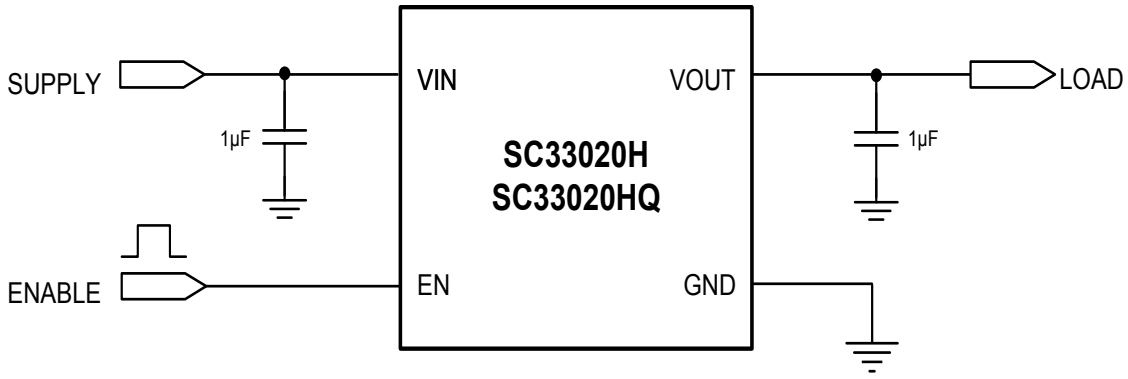
The SC33020H/HQ is an integrated 2A Load Switch with reverse current blocking. It operates from an 1.6V to 5.5V input supply. The SC33020H/HQ leaves the output node floating when the part is disabled.

The device provides extremely low 32mOhm ON resistance (R_{ON}) in an ultra-small package. The reverse blocking feature prevents current from flowing in reverse direction from the output through the device to the input supply rail. Whenever Vout to VIN voltage exceeds the reverse blocking threshold, reverse blocking is activated regardless of the IC enable state either (ON or OFF).

The SC33020H/HQ is offered in a tiny 0.9mm x 0.9mm x 0.60mm, 4 Bump CSP package. The SC33020H/HQ has an operating temperature range of -40°C to +85 °C.

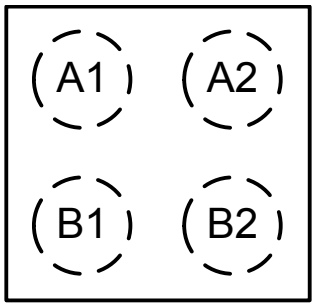
The SC33020HQ is an AEC-Q100 (Grade3)qualified version.

Typical Application Circuit



Pin Configuration

TOP VIEW
(BUMPS ON THE BOTTOM)



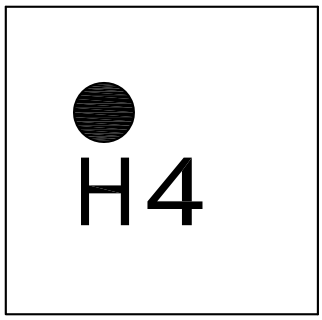
CSP 0.9x0.9, 4 Bump

Ordering Information

Device	Package
SC33020HCSTRT(1)(2)	CSP 0.9mm x 0.9mm 4-bump
SC33020HQCSTRT (1)(2)(3)	CSP 0.9mm x 0.9mm 4-bump
SC33020HCSEVB	Evaluation Board
SC33020HQCSEVB	Evaluation Board

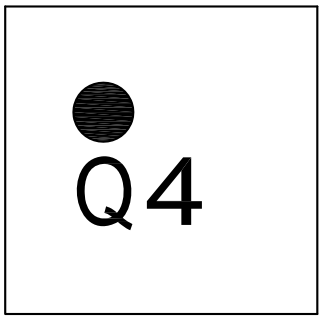
Notes:
(1) Available in tape and reel only. A reel contains 5,000 devices.
(2) Lead-free package only. Device is WEEE and RoHS compliant, and halogen free.
(3) Device is AEC-Q100 (Grade3) qualified.

Marking Information(SC33020H)



Marking for the 0.9 x 0.9 mm CSP 4 Lead Package :
O = Pin 1 ID
nn = Part No. Code (Example: H4) - Reference Part No.

Marking Information(SC33020HQ)



Marking for the 0.9 x 0.9 mm CSP 4 Lead Package :
O = Pin 1 ID
nn = Part No. Code (Example: H4) - Reference Part No.

Absolute Maximum Ratings

VIN to GND (V) ..... -0.3 to +6.0
EN to GND (V) ..... -0.3 to +6.0
VOUT to GND (V) ..... -0.3 to (V_{VIN} +0.3)
ESD Protection Level HBM (1) (kV) 4

Recommended Operating Conditions

Maxiumum Output Current (A) 2
V_{VIN} (V) ..... 1.6 to 5.5
Operating Ambient Temperature (°C) ..... -40 to 85

Thermal Information

Thermal Resistance, Junction to Ambient(2)
CSP (°C/W) 140
Maximum Junction Temperature (°C) ..... +125
Storage Temperature Range (°C) ..... -65 to +150
Peak IR Reflow Temperature (10s to 30s) (°C) ..... +260

Exceeding the above specifications may result in permanent damage to the device or device malfunction. Operation outside of the parameters specified in the Electrical Characteristics section is not recommended.

NOTES:
(1) Tested according to JEDEC standard JS-001-2012.
(2) Calculated from package in still air, mounted to 3 x 4.5 (in), 4 layer FR4 PCB with thermal vias under the exposed pad per JESD51 standards.

Electrical Characteristics

Unless noted otherwise, T_A = 25°C for typical, -40°C ≤ T_A = T_J ≤ +85°C for min and max, V_{IN} = 2.8V, C_{IN} = 1µF, C_{OUT} = 1µF, V_{EN} = V_{VIN}.

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Ron	R_{ON}	V_{IN}=5.5V, I_{OUT} = 200mA, V_{EN} =1.5V		25		mΩ
		V_{IN}=4.3V, I_{OUT} = 200mA, V_{EN} =1.5V		28		mΩ
		V_{IN}=3.3V, I_{OUT} = 200mA, V_{EN} =1.5V		32	43	mΩ
		V_{IN}=2.5V, I_{OUT} = 200mA, V_{EN} =1.5V		40		mΩ
		V_{IN}=1.6V, I_{OUT} = 200mA, V_{EN} =1.5V		65		mΩ
Quiescent Current(1)	I_Q	V_{IN}=V_{EN},=3.6V, V_{OUT} = open		1	3	µA
Shutdown Current	I_{SD}	V_{EN}=0, V_{OUT} = open		0.3	2	µA
		V_{EN}=0, V_{OUT} = open, T_A = 25°C			1.3	µA
Turn-on Delay Time(2)	T_{DT}	V_{EN}=1.5V, V_{IN}=3.6V, I_{OUT}=200mA, C_{OUT}=1µF		440		µs
Rising Time(2)	T_{RT}	V_{EN}=1.5V, V_{IN}=3.6V, I_{OUT}=200mA, C_{OUT}=1µF		640		µs
Reverse Blocking						
Reverse Blocking Current (IOUT)	R_I	V_{EN}=0V, V_{IN}=0V, V_{OUT}=5.5V		3	13	µA
Reverse Blocking Threshold	R_{BT}	V_{OUT}=2.8V, V_{OUT} – V_{IN}		75		mV
Reverse Blocking Hysteresis	R_{BH}	V_{OUT}=2.8V, V_{OUT} – V_{IN}		25		mV

Electrical Characteristics (continued)

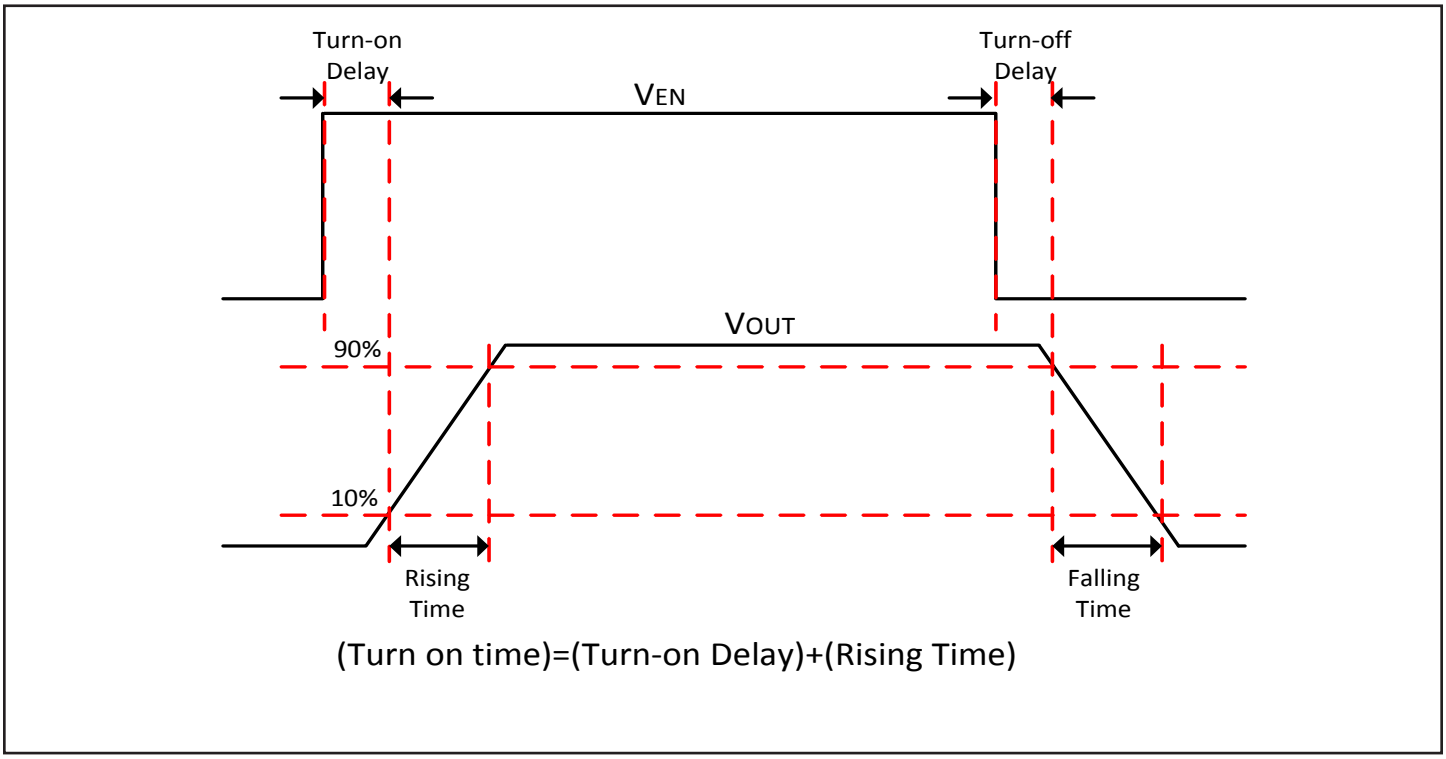
Parameter	Symbol	Conditions	Min	Typ	Max	Units
EN Digital Input						
EN Input High Threshold	V_{EN-IH}				1.0	V
EN Input Low Threshold	V_{EN-IL}		0.4			V
EN Input Pull-Down Resistance	R_{EN}			7		MΩ

NOTES:
(1) I_Q current includes EN pull-down current.
(2) For other T_{DT} and T_{RT} options, please contact SEMTECH.

Pin Descriptions

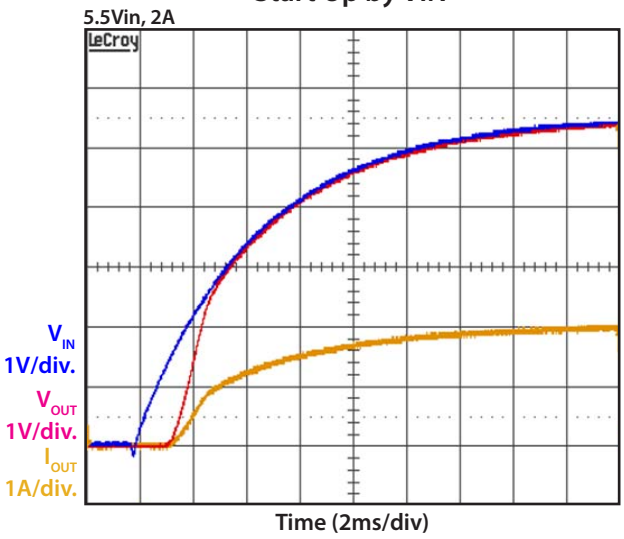
Pin#	Pin Name	Pin Function
A1	VIN	Input supply voltage.
B1	EN	Enable input. A 7MΩ internal resistor is connected from this pin to GND. Drive HIGH to turn on the switch; drive LOW to turn off the switch. When the EN pin is floated, the switch is OFF.
A2	OUT	Output voltage.
B2	GND	Ground.

Timing Diagram

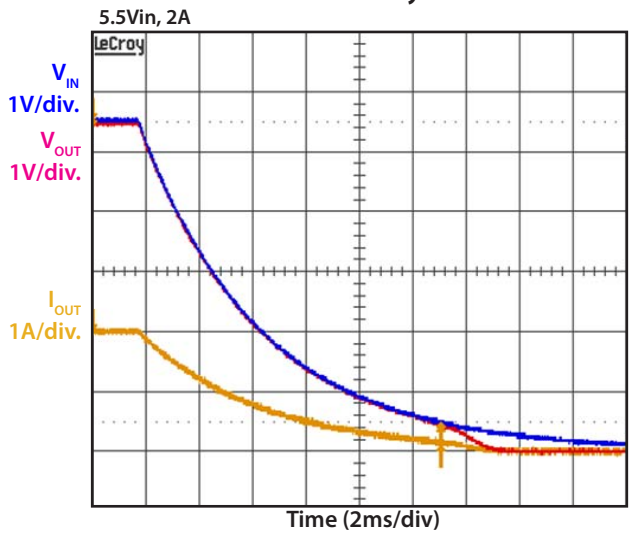


Typical Characteristics

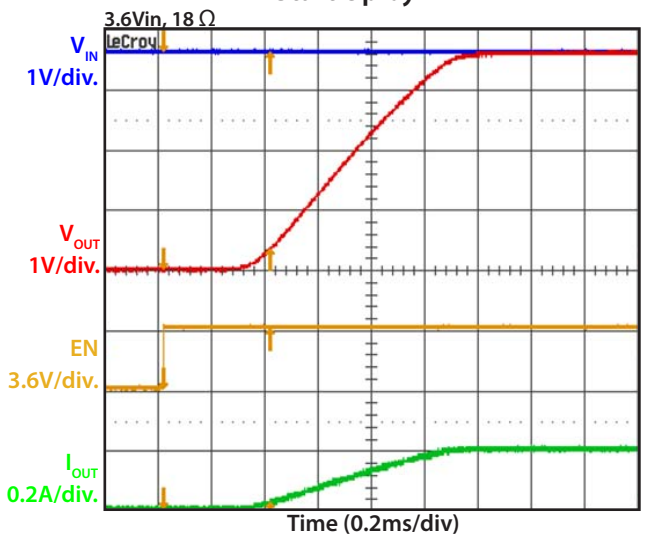
Start Up by VIN



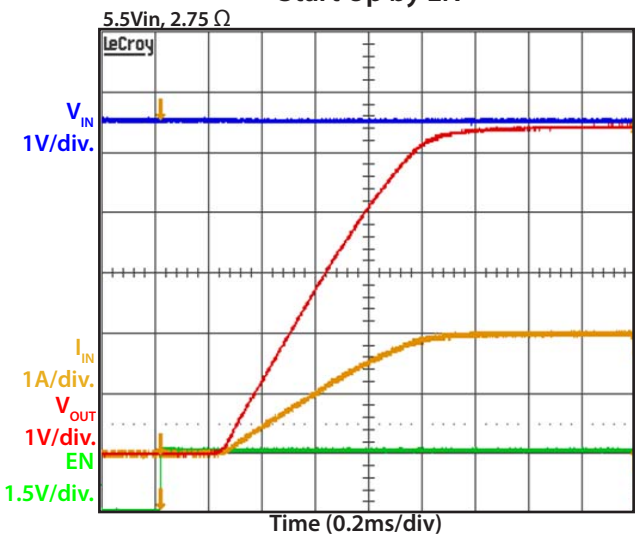
Shut Down by VIN



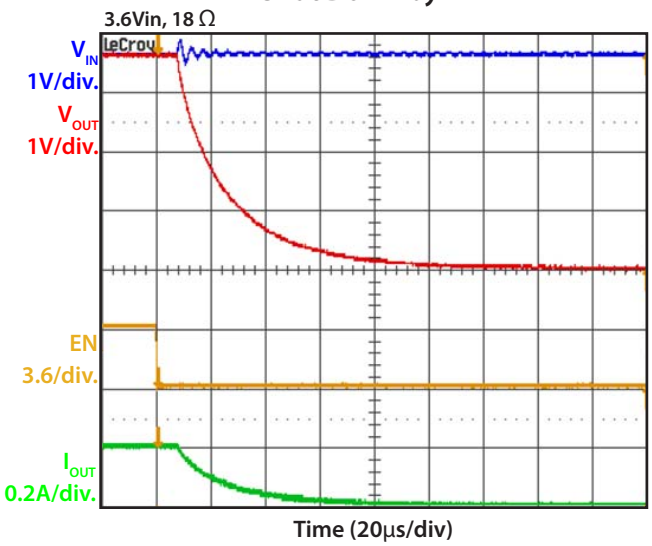
Start Up by EN



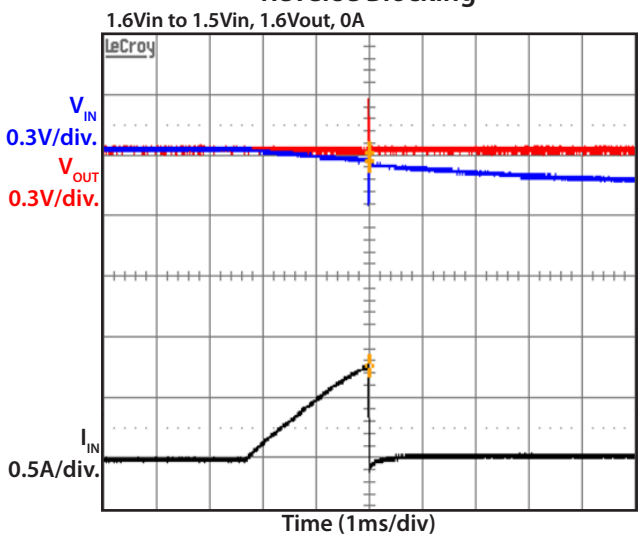
Start Up by EN



Shut Down by EN

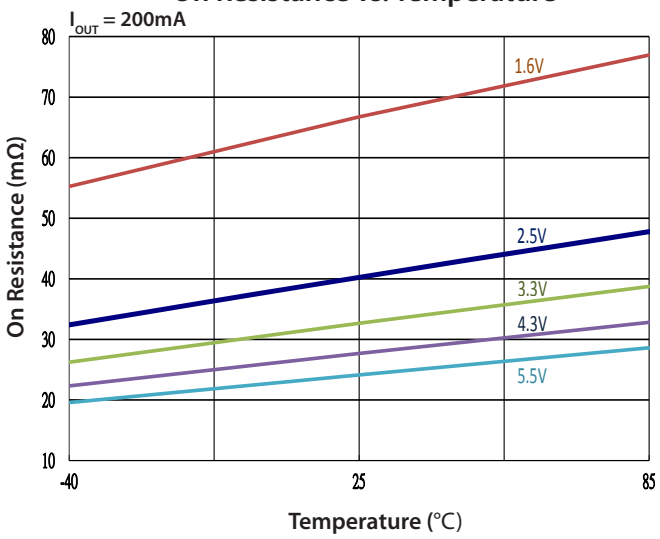


Reverse Blocking

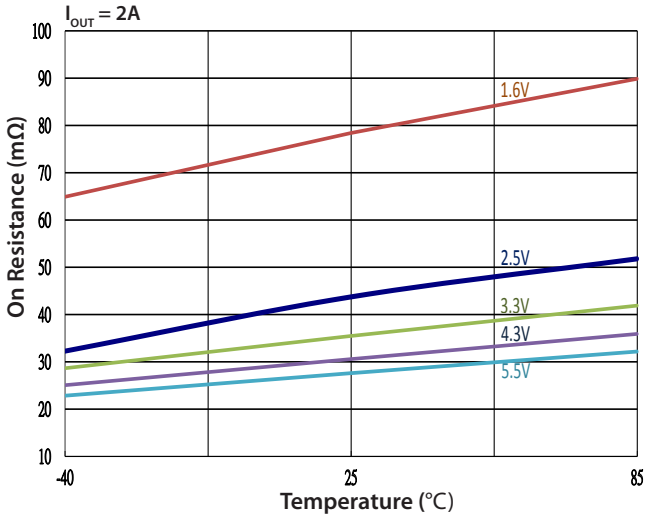


Typical Characteristics, Cont.

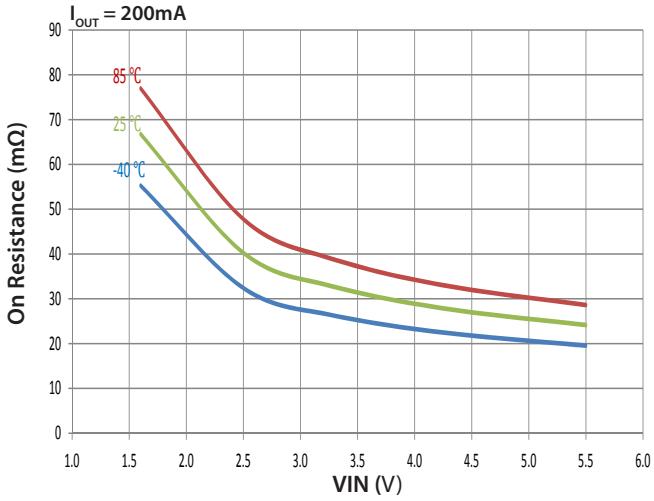
On Resistance vs. Temperature



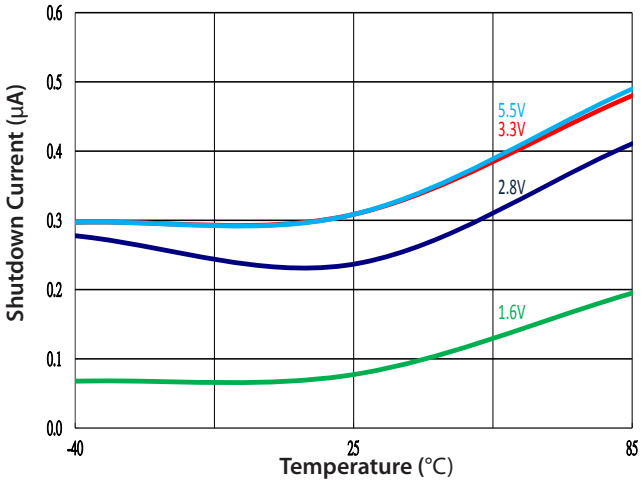
On Resistance vs. Temperature



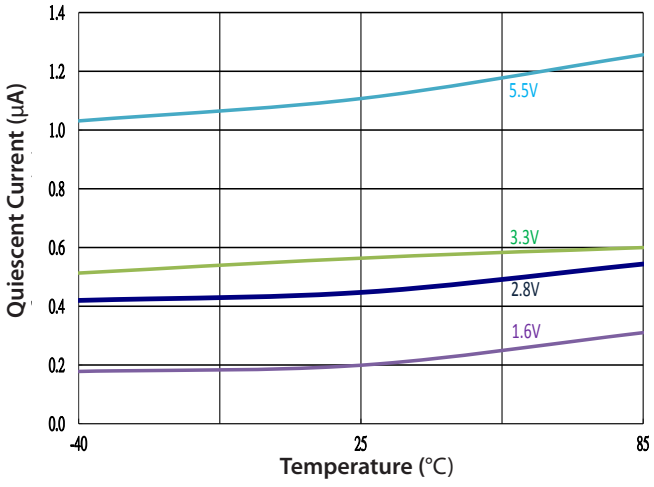
On Resistance vs. Input Voltage



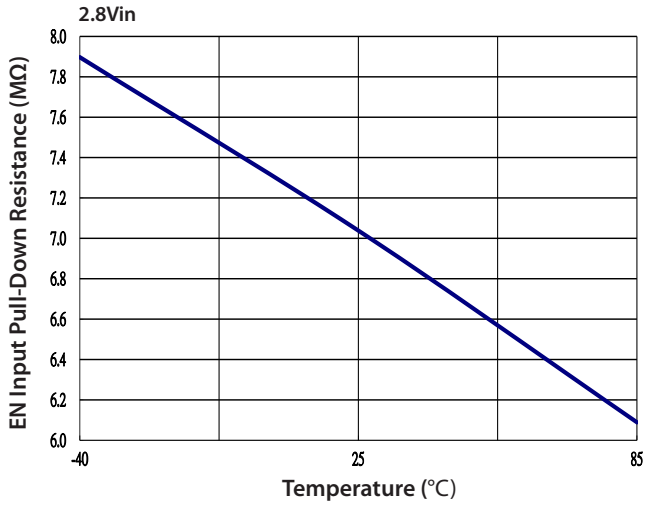
Shut Down Current vs. Temperature



Quiescent Current vs. Temperature

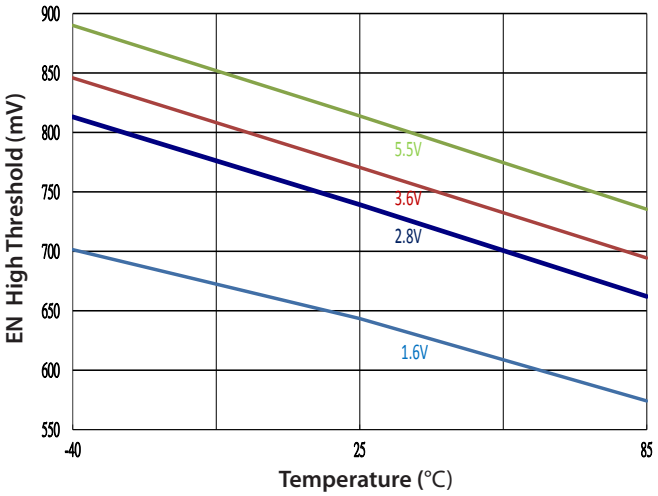


EN Pull-Down Resistance vs. Temperature

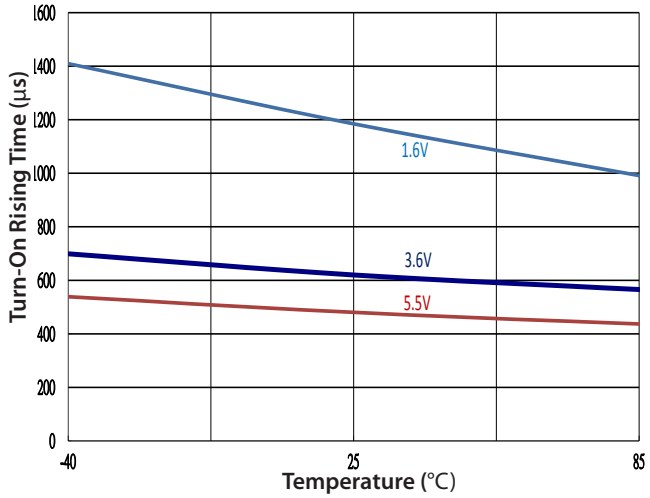


Typical Characteristics, Cont.

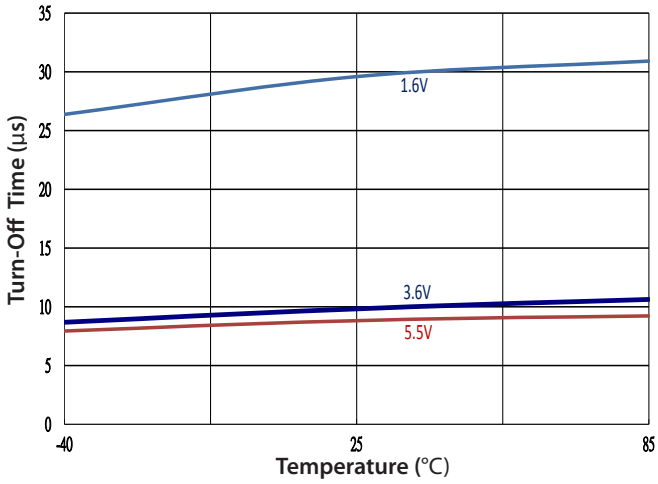
EN HIGH Threshold vs. Temperature



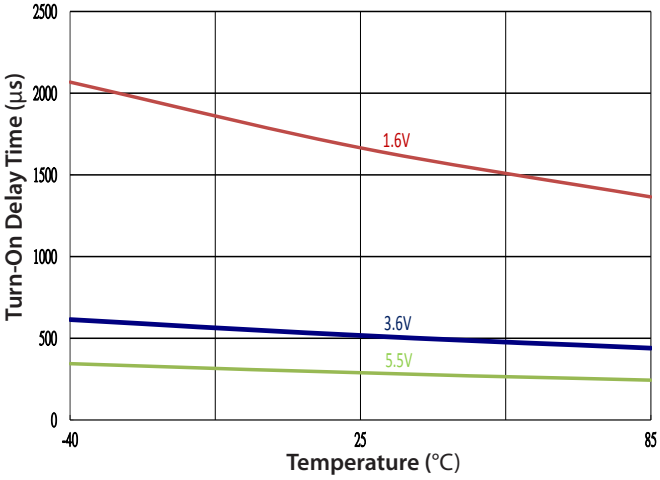
Rising Time vs. Temperature



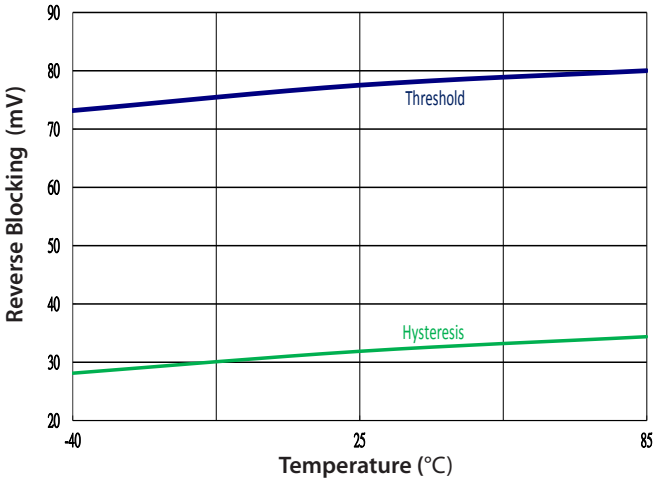
Turn-off Time vs. Temperature



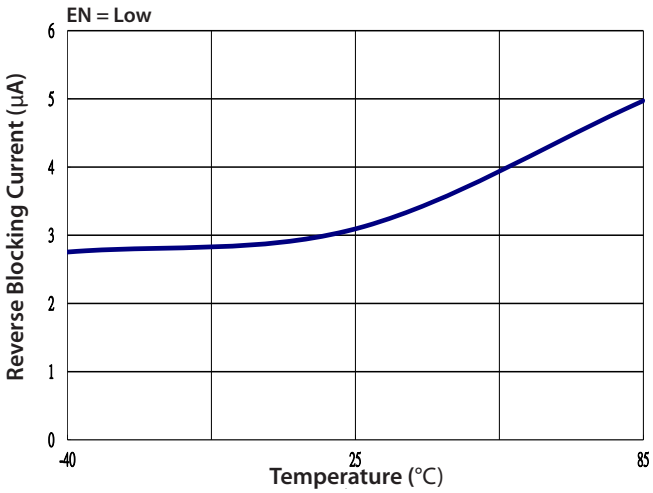
Turn-on Delay Time vs. Temperature



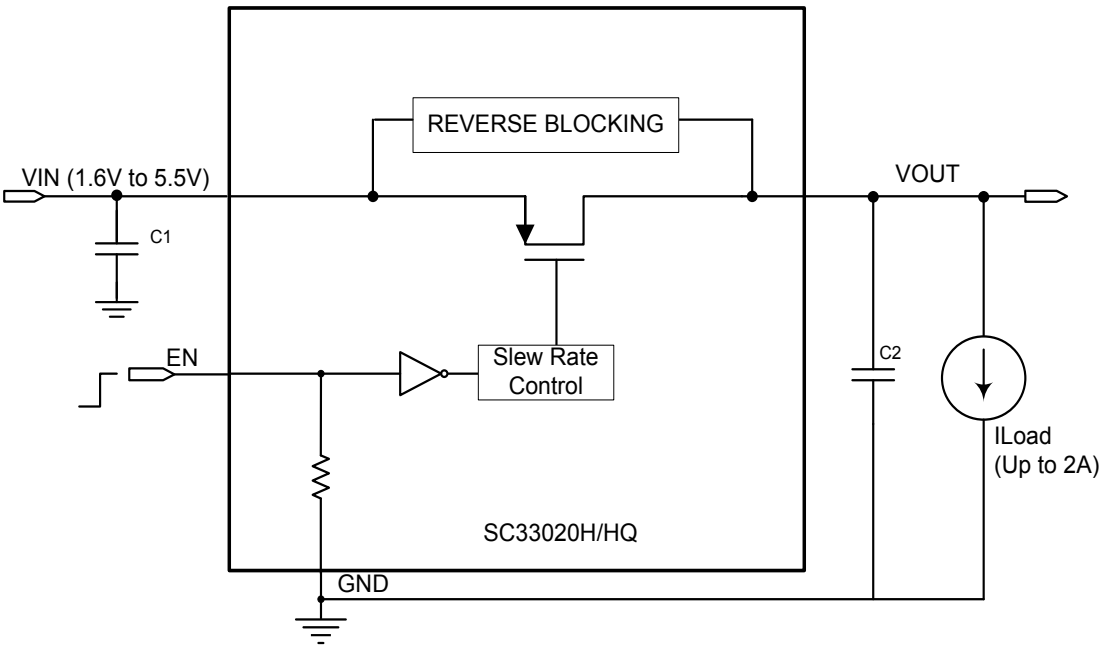
Reverse Blocking Threshold and Hysteresis vs. Temperature



Reverse Blocking Current vs. Temperature



Block Diagram



Application Information

Operation

The SC33020H/HQ is an integrated high-side PMOS load switch that is designed to support up to 2A continuous output current and operates from an input voltage from 1.6V to 5.5V. The internal PMOS pass element has a very low ON resistance of 32mΩ (typical) at V_{IN} = 3.3V. The SC33020H/HQ also provides ultra-low shutdown and quiescent current for extended battery life during shutdown and standby conditions.

An internal soft start circuit is used to control the start-up time of the load switch to reduce inrush current during start up.

Enable

The EN pin controls the ON/OFF state of the load switch. Pulling the EN pin HIGH turns on the load switch. Pulling the EN pin LOW turns off the load switch. The EN pin incorporates a 7MΩ (typical) pull-down resistor, so that when the EN pin is floating the SC33020H/HQ is disabled.

Reverse Blocking

The SC33020H/HQ integrates a reverse current blocking circuit to prevent current flow from VOUT to VIN during both ON and OFF states. The reverse current blocking circuit is active when voltage is present on either the VIN or the VOUT. A comparator is used to sense and compare the VOUT voltage to the VIN voltage. Whenever the VOUT voltage is 75mV (typical at 25°C) higher than VIN, the Reverse Blocking circuit is triggered and reverse current is blocked from VOUT to VIN. Please note that when 0 < VOUT - VIN < 75mV (typical at 25°C), some small reverse current is possible. An example is shown in Fig. 1. Usually, worst case for reverse current occurs at elevated input voltages and reduced temperatures.

The following formula can be used to calculate the reverse peak current before the Reverse Blocking circuit is triggered.

$$I_{RC} = \frac{V_{RBT}}{R_{ON}}$$

I_{RC} - Reverse peak current.
R_{ON} - On-Resistance, (Usually, is smaller resistance at higher V_{IN} and lower temperature.)
V_{RBT} - Reverse voltage threshold.

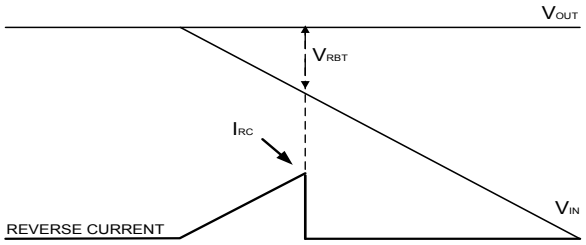


Figure 1

Input Capacitor

In order to reduce the effect of voltage drop, noise and bounce at the VIN pin, a filter/decoupling capacitor between VIN to GND is recommended. A 1µF ceramic capacitor is sufficient for most application conditions. However, it should be noted that suppressing bounce at input loop after EN is changed from HIGH to LOW can require greater capacitor values depending on particular designs.

In circuit design, ceramic capacitors should be derated for operating temperature and voltage. For applications up to 3.6V, capacitors should be rated at 6.3V or higher. For applications up to 5.5V, capacitors should be rated at 10V or higher.

Output Capacitor

A 1µF ceramic capacitor is normally used at the VOUT pin to filter noise. If a larger output capacitance value is used, the input inrush current should be considered because the power-on transient is also dependent on the output capacitor value. Please use the same derating criteria for the output capacitor selection.

Board Layout Considerations

Fig. 2 shows a typical application circuit with PCB inductance on the circuit board. An important objective of the layout is to minimize the PCB inductance by reducing the length and increasing the width of the traces. PCB inductance can affect circuit performance during turn-off, load transients, and Reverse Blocking. Fig. 2 shows three current loops during the opening or closing of the load switch. The magnitude of the voltage ringing at VIN or VOUT pin

Application Information, Cont.

is related to the PCB stray inductance and the placement of the capacitors. The input capacitor C1 and output capacitor C2 need to be placed close to the SC33020H/HQ. It is important to keep the voltage ringing below the maximum voltage rating of the SC33020H/HQ.

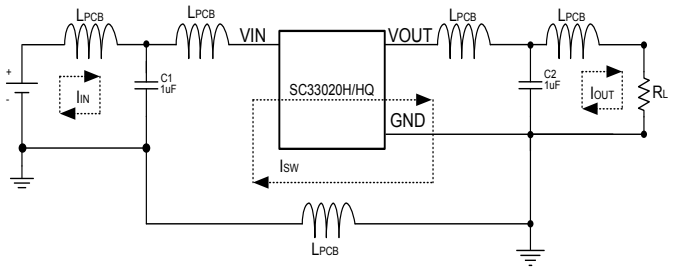


Figure 2 - PCB Circuit with Equivalent Parasitic Inductance

Evaluation Board Information

The Top Layer and Bottom Layer of a standard evaluation board are shown in Fig. 3 and Fig. 4, respectively.

Both T1 and T2 test points are Kelvin connections which can be used to minimize the measurement error of $R_{ON.}$ To enable the part, a jumper can be used between VIN and EN on J1. To disable the part, a jumper can be connected between EN and GND on J1. C3 is an optional solution to improve ringing at input rail during turn-off and reverse blocking conditions.

Top Layer, SC33020H

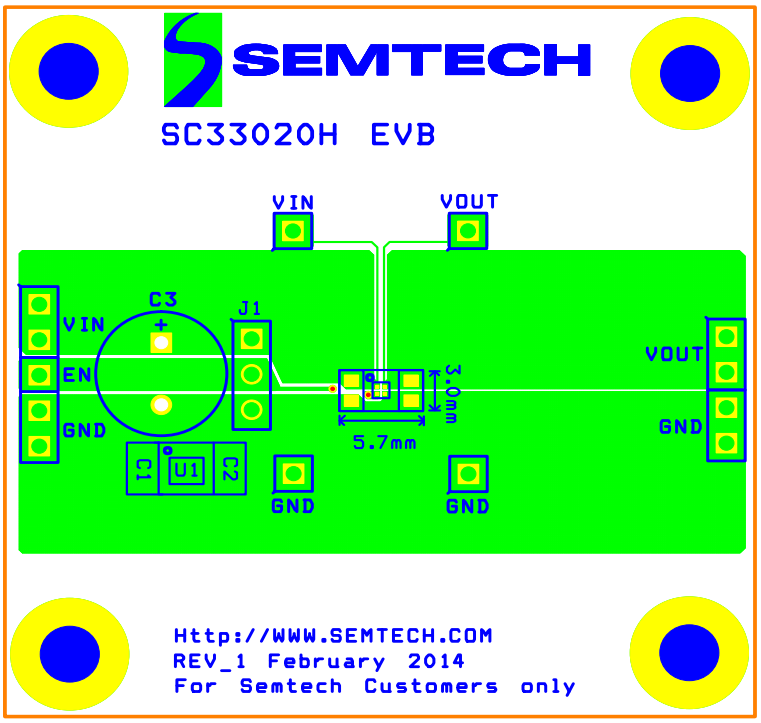


Figure 3

Bottom Layer, SC33020H

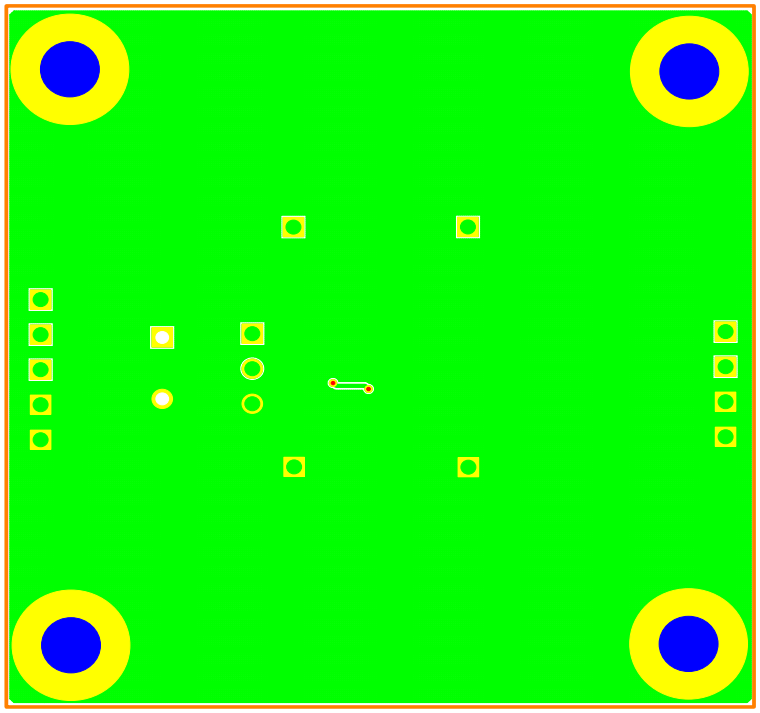


Figure 4

Top Layer, SC33020HQ

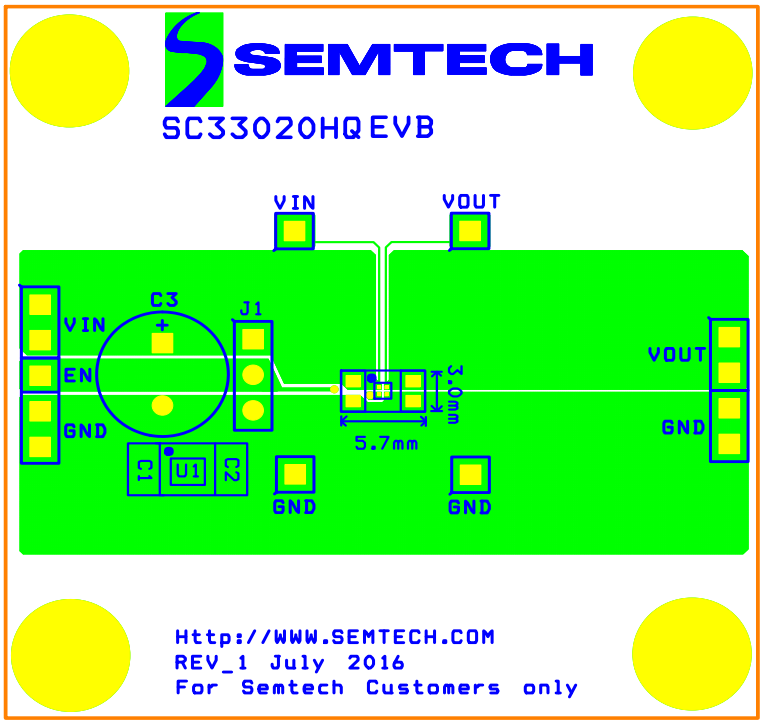


Figure 5

Bottom Layer, SC33020HQ

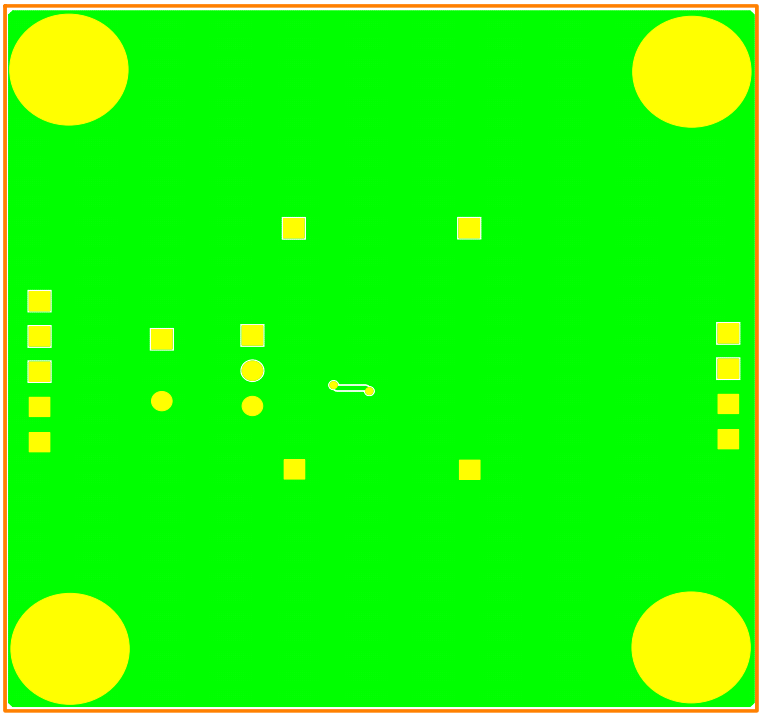
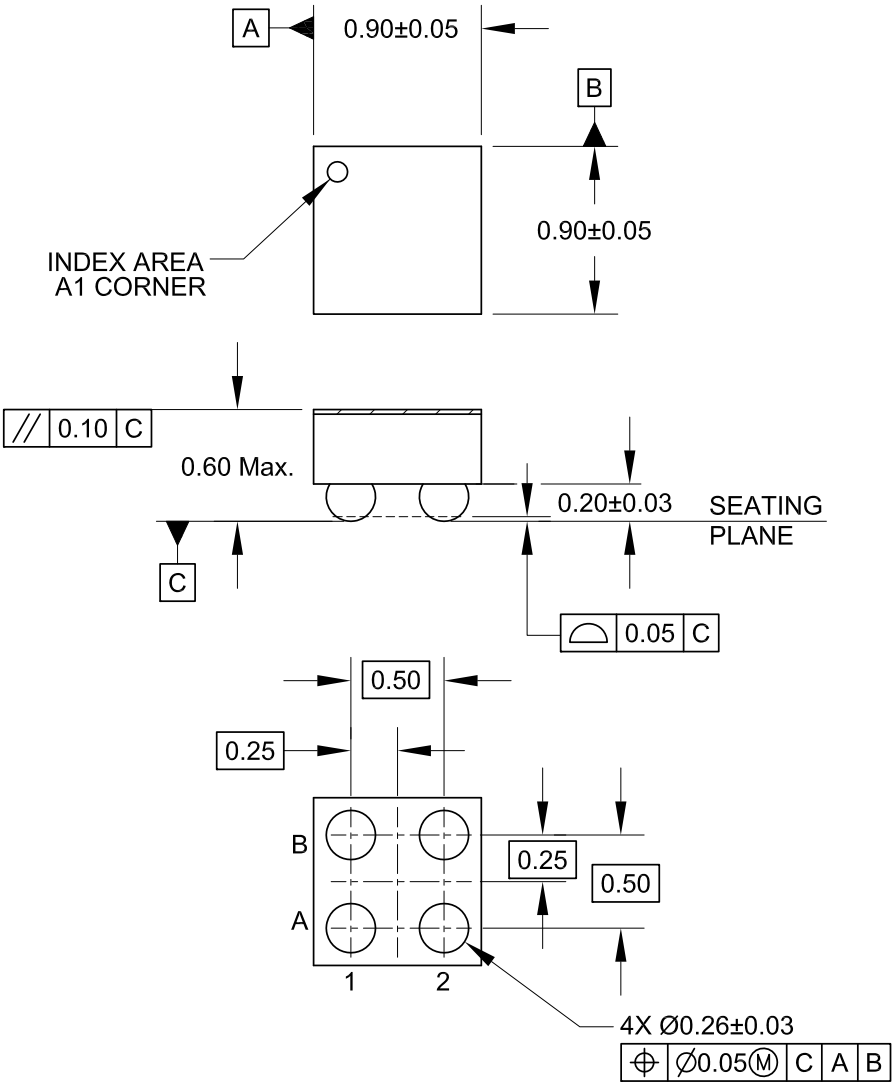


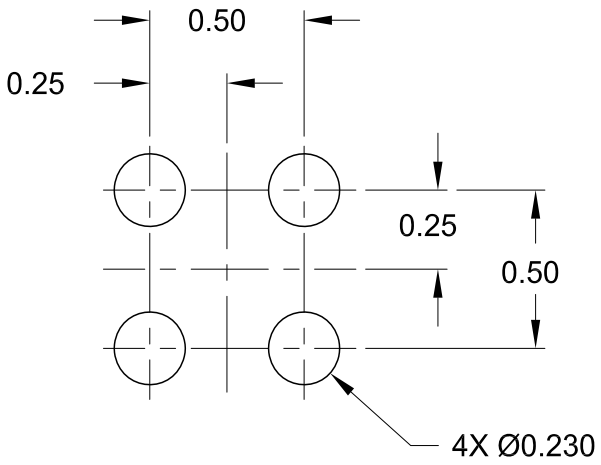
Figure 6

Outline Drawing — CSP 0.9mm X 0.9mm, 4 Lead



NOTES:
1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS

Land Pattern — CSP 0.9mm X 0.9mm, 4 Lead



NOTES:

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS
2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.

Contact Information

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