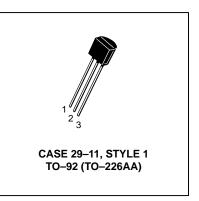
Switching Transistors NPN Silicon

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCEO	15	Vdc
Collector-Emitter Voltage	VCES	40	Vdc
Collector-Base Voltage	VCBO	40	Vdc
Emitter-Base Voltage	VEBO	4.5	Vdc
Collector Current — Continuous	IC	200	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	625 5.0	mW mW/°C
Operating and Storage Junction Temperature Range	TJ, T _{Stg}	-55 to +150	°C

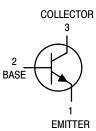


*ON Semiconductor Preferred Device



THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	200	°C/W



ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic		Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Collector–Emitter Breakdown Voltage(1) ($I_C = 10 \text{ mAdc}, I_B = 0$)	MPS2369A	V(BR)CEO	15	—	—	Vdc
Collector–Emitter Breakdown Voltage $(I_C = 10 \ \mu Adc, \ V_{BE} = 0)$	MPS2369,A	V _(BR) CES	40	—	—	Vdc
Collector–Base Breakdown Voltage $(I_C = 10 \ \mu Adc, I_E = 0)$	MPS2369,A	V _(BR) CBO	40	—	—	Vdc
Emitter–Base Breakdown Voltage (I _E = 10 μ Adc, I _C = 0)	MPS2369,A	V _{(BR)EBO}	4.5	—	—	Vdc
Collector Cutoff Current $(V_{CB} = 20 \text{ Vdc}, I_E = 0)$ $(V_{CB} = 20 \text{ Vdc}, I_E = 0, T_A = 125^{\circ}C)$	MPS2369,A	ICBO		_	0.4 30	μAdc
Collector Cutoff Current (V _{CE} = 20 Vdc, V _{BE} = 0)	MPS2369,A	ICES	—	—	0.4	μAdc

1. Pulse Test: Pulse Width \leq 300 µs, Duty Cycle \leq 2.0%.

Preferred devices are ON Semiconductor recommended choices for future use and best overall value.

MPS2369 MPS2369A

ELECTRICAL CHARACTERISTICS (T _A = 2	5°C unless otherwise noted) (Continued)
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Characteristic		Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS						
$\label{eq:constraint} \begin{array}{l} \text{DC Current Gain(1)} \\ (I_{C} = 10 \text{ mAdc}, \text{V}_{CE} = 1.0 \text{ Vdc}) \\ (I_{C} = 10 \text{ mAdc}, \text{V}_{CE} = 1.0 \text{ Vdc}, \text{T}_{A} = -55^{\circ}\text{C}) \\ (I_{C} = 10 \text{ mAdc}, \text{V}_{CE} = 1.0 \text{ Vdc}) \\ (I_{C} = 10 \text{ mAdc}, \text{V}_{CE} = 0.35 \text{ Vdc}) \\ (I_{C} = 10 \text{ mAdc}, \text{V}_{CE} = 0.35 \text{ Vdc}, \text{T}_{A} = -55^{\circ}\text{C}) \\ (I_{C} = 30 \text{ mAdc}, \text{V}_{CE} = 0.4 \text{ Vdc}) \\ (I_{C} = 100 \text{ mAdc}, \text{V}_{CE} = 2.0 \text{ Vdc}) \\ (I_{C} = 100 \text{ mAdc}, \text{V}_{CE} = 1.0 \text{ Vdc}) \end{array}$	MPS2369A MPS2369 MPS2369 MPS2369A MPS2369A MPS2369A MPS2369 MPS2369A	hfe	 20 40 20 30 20 20		120 — 120 — — — —	_
Collector-Emitter Saturation Voltage ⁽¹⁾ ($I_C = 10 \text{ mAdc}, I_B = 1.0 \text{ mAdc}$) ($I_C = 10 \text{ mAdc}, I_B = 1.0 \text{ mAdc}$) ($I_C = 10 \text{ mAdc}, I_B = 1.0 \text{ mAdc}, T_A = +125^{\circ}C$) ($I_C = 30 \text{ mAdc}, I_B = 3.0 \text{ mAdc}$) ($I_C = 100 \text{ mAdc}, I_B = 10 \text{ mAdc}$)	MPS2369 MPS2369A MPS2369A MPS2369A MPS2369A	V _{CE(sat)}	 		0.25 0.20 0.30 0.25 0.50	Vdc
Base-Emitter Saturation Voltage ⁽¹⁾ ($I_C = 10 \text{ mAdc}, I_B = 1.0 \text{ mAdc}$) ($I_C = 10 \text{ mAdc}, I_B = 1.0 \text{ mAdc}, T_A = +125^{\circ}C$) ($I_C = 10 \text{ mAdc}, I_B = 1.0 \text{ mAdc}, T_A = -55^{\circ}C$) ($I_C = 30 \text{ mAdc}, I_B = 3.0 \text{ mAdc}$) ($I_C = 100 \text{ mAdc}, I_B = 10 \text{ mAdc}$)	MPS2369 MPS2369A MPS2369A MPS2369A MPS2369A	VBE(sat)	0.7 0.5 — —		0.85 — 1.02 1.15 1.60	Vdc
SMALL-SIGNAL CHARACTERISTICS		· · · · · ·		I	1	·
Output Capacitance (V _{CB} = 5.0 Vdc, I _E = 0, f = 1.0 MHz)	MPS2369,A	C _{obo}	_	_	4.0	pF
Small–Signal Current Gain (I _C = 10 mAdc, V _{CE} = 10 Vdc, f = 100 MHz)	MPS2369,A	h _{fe}	5.0	_	—	—
SWITCHING CHARACTERISTICS						
Storage Time ($I_{B1} = I_{B2} = I_C = 10 \text{ mAdc}$) (Figure 3)	MPS2369,A	ts	_	5.0	13	ns
Turn–On Time (V _{CC} = 3.0 Vdc, I _C = 10 mAdc, I _{B1} = 3.0 mAdc) (Figure 1)	MPS2369,A	ton	_	8.0	12	ns
Turn–Off Time (V _{CC} = 3.0 Vdc, I _C = 10 mAdc, I _{B1} = 3.0 mAdc, I _{B2} = 1.5 mAdc) (Figure 2)	MPS2369,A	^t off	_	10	18	ns

1. Pulse Test: Pulse Width \leq 300 $\mu s,$ Duty Cycle \leq 2.0%.

MPS2369 MPS2369A

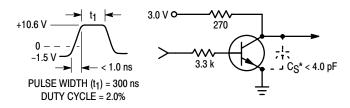


Figure 1. ton Circuit

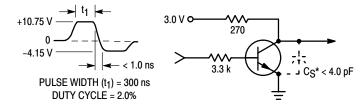


Figure 2. toff Circuit

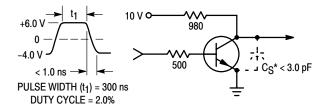
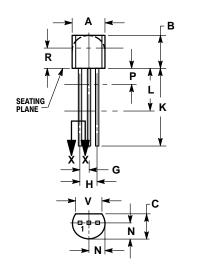


Figure 3. Storage Test Circuit

*Total shunt capacitance of test jig and connectors.

PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 **ISSUE AL**





NOTES

DIMENSIONING AND TOLERANCING PER ANSI 1

Y14.5M, 1982. CONTROLLING DIMENSION: INCH.

2 3.

CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.

LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Ρ		0.100		2.54
R	0.115		2.93	
۷	0.135		3.43	

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