



PIN	FUNCTION
1	Tri-state
2	GND
3	Output
4	VDD

Note: : It is recommended to use an approximately 0.01uF bypass capacitor between PIN 2 and 4.

UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCH(MM) SURFACE FINISH: TOLERANCES: LINEAR: ANGULAR:			FINISH:				DEBUR AND BREAK SHARP EDGES	D	NOT SCALE DRAWING REVISION -		
								ABRACON			
\vdash	NAME	SIGNATURE		DATE					CORPORATION		
DRAWN	SAAZVAT							30332 E	eneranza Rancho San	cho Santa margarita. California 02688	
CHK'D	XXXXXX							30332 Esperanza, Rancho Santa margarita, California 92688			
APPV'D								TITLE:		TOD	
MFG	ĺ							1	OSCILLA	ATOR	
Q.A					MATERIAL	L:		DWG NO.			4.2
									ASFLK	K	A3
					1			\sim 3		_IX	
					WEIGHT:			SCALE-15-1		SHEET 1 OF 1	

TOP PACKAGE MARKING IS FOR ILLUSTRATION PURPOSES ONLY